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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/709,425	05/05/2004	Jie-Hong Wang	FTCP0029USA	3424
27765	7590 12/19/2005		EXAMINER	
NORTH AMERICA INTELLECTUAL PROPERTY CORPORATION P.O. BOX 506			CHARIOUI, MOHAMED	
	MERRIFIELD, VA 22116			PAPER NUMBER
				5

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
Office Action Summary	10/709,425	WANG ET AL.			
	Examiner Mehamod Charievi	Art Unit			
The MAILING DATE of this communication ap	Mohamed Charioui pears on the cover sheet wi	th the correspondence address			
Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING D - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNION (136(a). In no event, however, may a red will expire SIX (6) MON te, cause the application to become AB	CATION. eply be timely filed THS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on 05 M	<u>Иау 2004</u> .				
2a) ☐ This action is FINAL . 2b) ☑ Thi	This action is FINAL . 2b)⊠ This action is non-final.				
3) Since this application is in condition for allowa	•	•			
closed in accordance with the practice under	Ex parte Quayle, 1935 C.D	o. 11, 453 O.G. 213.			
Disposition of Claims					
4) Claim(s) 1-11 is/are pending in the application	☑ Claim(s) <u>1-11</u> is/are pending in the application.				
4a) Of the above claim(s) is/are withdra	awn from consideration.				
5) Claim(s) is/are allowed.					
6)⊠ Claim(s) <u>1-4 and 6-11</u> is/are rejected.					
7) Claim(s) 5 is/are objected to.					
8) Claim(s) are subject to restriction and/	or election requirement.				
Application Papers					
9) The specification is objected to by the Examin	er.				
10)⊠ The drawing(s) filed on <u>05 May 2004</u> is/are: a)⊠ accepted or b)□ object	cted to by the Examiner.			
Applicant may not request that any objection to the	- · ·	· ·			
Replacement drawing sheet(s) including the correct		• •			
11) The oath or declaration is objected to by the E	xaminer. Note the attached	d Office Action or form PTO-152.			
Priority under 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 		119(a)-(d) or (f).			
2. Certified copies of the priority documen		pplication No			
3. Copies of the certified copies of the price	ority documents have been	received in this National Stage			
application from the International Burea	, , , , ,				
* See the attached detailed Office action for a list	t of the certified copies not	received.			
Attachment(s)					
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	, —	Summary (PTO-413) s)/Mail Date			
 Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date 		nformal Patent Application (PTO-152)			

Art Unit: 2857

DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claim 6 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 6 recites "A device for implementing the method of claim 1." Claim 6 is considered vague and indefinite because it limits the method in parent claim 1 with a specific apparatus, thereby incorporating both product and method subject matter.

Claim Rejections - 35 USC § 101

2. 35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

Claim 6 is rejected under 35 U.S.C. 101 because the claimed invention is directed to non-statutory subject matter.

Claim 6 is rejected under 35 U.S.C. 101 since the claim is directed to neither a "process" nor a "machine" but rather embraces or overlaps two different statutory classes of inventions set forth in 35 U.S.C. 101 which is drafted so as to set forth the statutory classes of invention in the alternative only.

Claim Rejections - 35 USC § 103

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3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-4 and 6-11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Khan (U.S. 5,377,203) in view of West (U.S. 6,940,271).

Khan teaches obtaining the test program of the chip tested on the test platform (see col. 6, line 63 to col. 7, line 2 and col. 7, lines 32-52); obtaining pin data of the chip by the test program of the chip (see col. 7, lines 33-42); setting up an input pin of the chip with an initial value; and providing a test current to the pin of the chip (see col. 6, lines 40-63 and col. 7, lines 33-51).

Khan does not explicitly teach measuring the current between a power end and a ground end of the chip to see if it exceeds a first predetermined value.

West teaches this feature (see col. 10, lines 41-66 and col. 8, lines 1-13). It would have been obvious to one having ordinary skill in the art at the time the invention was made to incorporate West teaching into Khan's invention because it would determine if there is current leakage in the chip. Therefore, appropriate action would be taken to prevent the damage of the chip.

Allowable Subject Matter

4. Claim 5 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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The following is a statement of reasons for the indication of allowable subject matter: none of the prior art of record teaches or suggests determining that the chip pass the latch-up test if the test current used in step (e) exceeds the second predetermined value, and the current between power end and the ground end of the chip does not exceed the first predetermined value.

Prior art

5. The prior art made record and not relied upon is considered pertinent to applicant's disclosure:

Tsujii ['593] discloses tester and method for testing LSI designed for scan method.

Gillette ['438] discloses current sense circuit.

Sunter ['921] discloses method and circuit for testing DC parameters of circuit input and output nodes.

Chun et al. ['676] disclose semiconductor chip ground noise immunity testing system and tester.

Yung ['202] discloses testing multiple IC parallel by a single tester.

Contact information

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mohamed Charioui whose telephone number is (571) 272-2213. The examiner can normally be reached Monday through Friday, from 9 am to 6 pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Marc S Hoff can be reached on (571) 272-2216. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mohamed Charioui

12/1/05

TECHNOLOGY CONTENT AND

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